

AS5047P to VESC motor controller

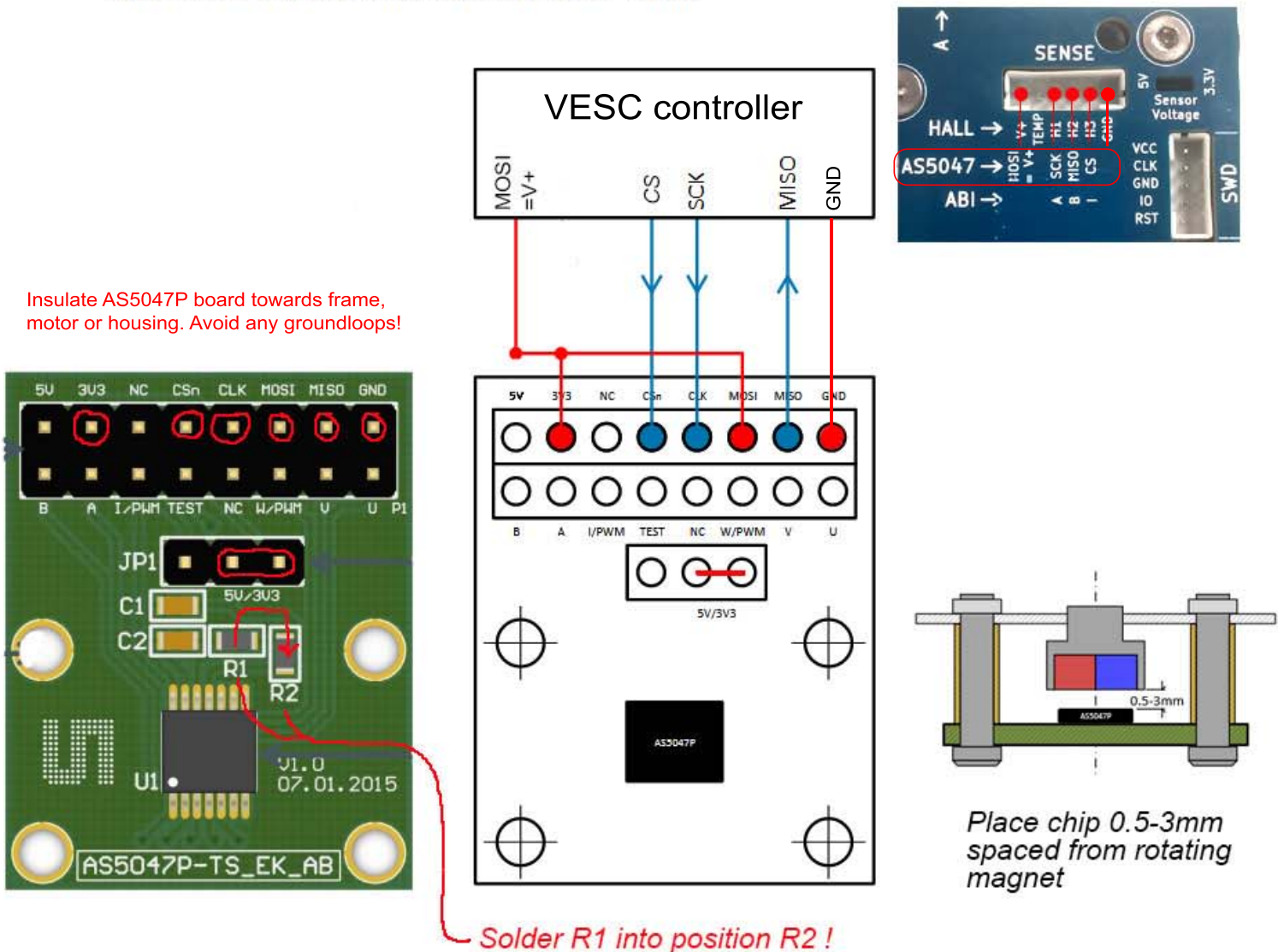
3.3V operation (default)

The minimum connection requirements for unidirectional communication between the microcontroller and the AS5047P are MISO, CLK, CSn. In this case the MOSI pin is tied to VDD which will result in reading only the 14-bit Angle Register (0x3FFF). See AS5047P datasheet register table, register 0x3FFF.

The connector JP1 allows to select between 5V or 3.3V operation.

R1 and R2 are 0 ohm resistors in 0603 package. Depending on the supply voltage either R1 or R2 has to be populated. For 5V operation R1 has to be populated and R2 has to be removed (default case). Vice versa for 3.3V operation.

Figure 5: One device SPI mode, unidirectional – 3 wire



AS5047P to VESC motor controller

3.3V operation (default)

The minimum connection requirements for unidirectional communication between the microcontroller and the AS5047P are MISO, CLK, CSn. In this case the MOSI pin is tied to VDD which will result in reading only the 14-bit Angle Register (0x3FFF). See AS5047P datasheet register table, register 0x3FFF.

The connector JP1 allows to select between 5V or 3.3V operation.

R1 and R2 are 0 ohm resistors in 0603 package. Depending on the supply voltage either R1 or R2 has to be populated. For 5V operation R1 has to be populated and R2 has to be removed (default case). Vice versa for 3.3V operation.

Figure 5: One device SPI mode, unidirectional – 3 wire

